

DIVYESH PATEL

Contact Details

P +61 44 99 11 207

E pateldivyesh6859@yahoo.in, patel.divyesh99 (Skype ID)

8/2-4, Cambridge Street, Harris Park
Sydney NSW 2150, Australia.

Objective/Overview

Multi skilled, dynamic, adaptive EC/IT engineer, expertise in electronics circuits and systems, component, devices/system testing, software development and hardware design. Major areas of interest are radio and satellite communication, robotics and also skilled in sub-fields of electronics such as control engineering, instrumentation, signal processing, information technology and telecommunication engineering.

Tertiary Qualification

Master of Information Technology (CQ University, Australia) 2015-2017

- Major: Software Design and Development Learning Outcomes

Bachelor of Technology (CHARUSAT University, India) 2009-2013

- Major: Electronics and Communication, CPI: 9.04
- Part of IETE Council
- Founder of iClub, CHARUSAT

Relevant Professional Experience

Electronics Engineer, pursuing (Elf Electronics, Sydney) 2016 (present)

- Insertion, Soldering, Verification/Repair, Testing, Final Assembly (ongoing project - Sydney Trains)

Research Associate (Indian Space Research Organisation (ISRO))-CHARUSAT Uni, India) 2013-2015

• **Skills developed:**

- Designed scientific experimental payloads for IMS-1 spacecraft
- Electronics circuit designing and testing, soldering (SMD, Through hole)
- Wi-Fi Communication Link (High Band, 5GHz), Antenna design (H/W & S/W),
- ZigBee Technology (2.4 GHz ISM Band, H/W & S/W),
- High speed communication using Optical Fiber & Power system design
- Recognising and repairing of board level components/ICs and their packages/dimensions, Assembling control systems
- Detailed understanding of electronics systems and manufacturing processes

- Proficiency and hands on experience of microcontroller boards
- Expertise in designing hardware, firmware level design and maintenance
- Strong background in software engineering, Big Data, Agile project management, latest web technologies
- Good at digital/software logic design
- Understanding of in-house software applications and R/DBMS
- Recent experience of MySQL, application s/w design

➤ **Professional Skills**

- Good Communicator
- Leadership, Innovative
- Planning and organising
- Problem solving skills

Electronics/Communication/Information Technology Projects

- Build online customer internet usage calculation application using java (NetBeans, Eclipse)** 2015
- Designed Application of best deal mobile selling/leasing company using SQL (Ms Access)** 2015
- Wireless Energy Meter** 2013
- PIC32MX575F256L,explorer 16 Boot Loader board,RFM73 wireless module,MCP3909 power measurement IC, Protocols(SPI,USB) Software: MPLAB v 8.76
 - Peripheral interfacing (LCD, LED, Keypad), Memory interfacing (EEPROM-25LC256) Energy Measurement using MCP3909, successfully completed (graduation final year).
- International Robotics Challenge, I NEXUS, IIT Bombay** 2012
- ARDUINO MEGA 2560 programming (c language), RF Communication, Servo arm pick and place
 - Photo sensors inefficient response, rewarded with best design award
- Speed Control System of DC Motor using Arduino 2560 MEGA and VISUAL BASICS** 2012
- Auto Load Balancing of the vehicle on sloppy region, close loop control system.
- Multipurpose functioning with LCD and KEYPAD using controller ATMEL 89C051** 2011
- Password authentication, Automatic locking system, 16 x 4 LCD interfacing, Digital Clock and Day, Assembly language programming in AT89C51 microcontroller
 - Automatic locking whenever not in use, Password Entry (limited attempts), Digital clock (24hour format) and day with alarm setting a like as mobile and indication of alarm with LED whenever interrupt occurs.

Technical/Computer Skills

- **Languages known:** C, C++, Java, VHDL, SQL/MySQL, JavaScript, XML, HTML, CSS
- **Operating Systems Proficiency:** Windows 7/8, XP, FEDORA 17, Ubuntu 10.0, Windows Vista, Linux
- **Software Platforms used:** Multisim, MATLAB, HFSS, LABVIEW, CADSTAR, MP LAB, Keil, Cisco Packet Tracer, Altera max Plus II, Proteus, OrCAD 16, Omnet ++, NS2, HFSS
- **Hardware Platforms used:** AT89C51, PIC32, MSP430, ARDUINO (UNO and MEGA), FPGA (Spartan 3E/6), and ARM 7
- **Development/Design:** Software development, debugging, simulation, Hardware designing and testing/interfacing
- **Analytical skills:** Hardware debugging, and fault finding, problem solving skills, troubleshooting h/w (phone, tablets, laptop)
- **Computer Proficiency:**
 - MS Word, MS Excel, MS Power Point , MS Access, MS Project, MS Office
 - Internet & Research & E-mail
 - Outlook expresses, Exchange, Photo Flash

Awards/Achievements

- Awarded with “Saraswati Sanmanit Award” in higher education, in science stream, std 11th & 12th (2009)
- Founder of iClub (Innovative club, build platform for international events participation and exploring technical skills) (2012)
- Member of IETE (Institution of Electronics and Telecommunication Engineers) CHARUSAT chapter (volunteer for events management) (2013)
- Achieved Vice Chancellor Scholarship for MIT, CC54 program (2015)
- Guide of Bachelor and Master students for final year projects (2015)
- Student Mentor (from term 1, 2016, CQUniversity, Sydney) (2016)
- Member of Space Generation Advisory Council (SGAC), project member of active SSS project (Low Power Electrical Plasma Thruster for CubeSats) July – 2016
- Member of Space Industry Association of Australia (SIAA) 2016

Hobbies and Special Interests

- Sports, Creative in Art, Driving and Travelling

Referees

- Available upon request